

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	3238	((address near4 transit\$3 near4 detect\$3) ATD)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/13 13:23
S2	0	((sense adj2 (amp amplifier)) with (reset adj3 signal)) and ((control\$5 adj3 circuit) with set\$4 with latch) and ((control\$5 adj3 circuit) with reset\$4 with latch)) and (((address near4 transit\$3 near4 detect\$3) ATD))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/13 13:23
S3	24	((sense adj2 (amp amplifier)) with (reset adj3 signal)) and ((control\$5 adj3 circuit) with set\$4 with latch) and ((control\$5 adj3 circuit) with reset\$4 with latch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/13 14:21
S4	135	((control\$5 adj3 circuit) with (set\$4 adj4 latch)) and ((control\$5 adj3 circuit) with (reset\$4 adj4 latch))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/13 14:22
S5	8	((address near4 transit\$3 near4 detect\$3) ATD)) and (((control\$5 adj3 circuit) with (set\$4 adj4 latch)) and ((control\$5 adj3 circuit) with (reset\$4 adj4 latch)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/11 10:59
S6	5312	((control\$5 adj3 circuit) with (set reset) with ((flip adj flop) latch))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/11 11:00
S7	37	((control\$5 adj3 circuit) with (set reset) with ((flip adj flop) latch))) and ((sense adj2 (amp amplifier)) with generat\$3 with (reset adj3 signal))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/11 11:04
S8	9	control\$4 same ((latch (flip adj flop)) with set) same ((latch (flip adj flop)) with reset) same ((latch (flip adj flop)) with (metastable (meta adj stable)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/11 11:16
S9	10	((control\$5 adj3 circuit) with (set reset) with ((flip adj flop) latch))) and (output with (metastab\$5 (meta adj stab\$5)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/11 11:25

S10	4560	(latch (flip adj flop)) with set with reset with (active high hi asserted on)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/11 11:28
S11	401	(latch (flip adj flop)) with (set near3 reset) adj4 (active high hi asserted on)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/11 11:28
S12	401	(latch (flip adj flop)) with ((set near3 reset) adj4 (active high hi asserted on))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/11 11:53
S13	0	((latch (flip adj flop)) with ((set near3 reset) adj4 (active high hi asserted on))) same (metastab\$5 (meta adj stab\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/11 11:29
S14	6	((latch (flip adj flop)) with ((set near3 reset) adj4 (active high hi asserted on))) and (metastab\$5 (meta adj stab\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/11 11:54
S15	1616	(latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/11 11:54
S16	27	((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/11 11:54
S17	21	((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))) and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/11 11:54
S18	6	((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))) not (((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))) and memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/11 11:54

S19	21	(((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))) not (((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))) not (((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))) and memory))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/11 11:54
S20	355	(sense adj (amp amplifier)) with (reset adj3 signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/11 14:08
S21	14	((sense adj (amp amplifier)) with (reset adj3 signal)) with (read\$3 with cell)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/11 14:08
S22	133	(sense adj (amp amplifier)) with generat\$3 with (reset adj3 signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/11 14:08
S23	25	(sense adj (amp amplifier)) near5 generat\$3 near5 (reset adj3 signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/11 14:12
S24	11	(sense adj (amp amplifier)) adj5 generat\$3 adj5 (reset)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/11 14:27
S25	80	(sense adj (amp amplifier)) adj5 (reset adj2 signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/11 14:27
S26	58	(sense adj (amp amplifier)) adj3 (reset adj2 signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/11 14:30
S27	24	(sense adj (amp amplifier)) with (reset adj2 signal) with (read\$3 adj2 operation)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/11 14:30

S28	6	(sense adj (amp amplifier) adj3 reset adj3 signal) with (controller (control adj (circuit unit)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/11 14:46
S29	14	(sense adj3 (amp amplifier)) near3 generat\$3 near3 (reset adj2 signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/10 15:03
S30	14	(sense adj3 (amp amplifier)) near3 generat\$3 near3 (reset adj2 signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/10 15:03
S31	64	(sense adj (amp amplifier)) adj3 (reset adj2 signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/10 15:05
S32	38	((((control\$5 adj3 circuit) with (set reset) with ((flip adj flop) latch))) and ((sense adj2 (amp amplifier)) with generat\$3 with (reset adj3 signal)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/10 15:05
S33	6	S30 and S31	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/10 15:06
S34	3	S31 and S32	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/10 15:08
S35	334	(sense adj3 (amp amplifier)) with reset with (latch (flip adj flop))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/10 15:14
S36	21	((((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))) not (((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))) not (((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))) and memory))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/10 15:12

S37	0	S35 and S36	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/10 15:11
S38	0	S31 and S36	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/10 15:13
S39	94	(sense adj3 (amp amplifier)) near3 reset near3 (latch (flip adj flop))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/10 15:39
S40	215	(sense adj3 (amp amplifier)) same reset same (latch (flip adj flop)) same set same read\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/10 15:25
S41	1	((sense adj3 (amp amplifier)) adj5 reset adj5 (latch (flip adj flop))) with (read\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/10 15:40
S42	2	"5740111".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/10 17:42